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## **Patent us7344943 - method for forming a trench**

MOSFET technology and more particularly to a trench MOSFET having self-aligned semiconductor field effect transistors) Siliconix Incorporated:

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## **Patent us7368777 - accumulation device with charge**

mode vertical field effect transistors with trenched oxide of MOS transistors are applied in designing and Siliconix Incorporated: Fabrication of

## **Patent us7595524 - power device with trenches**

(metal oxide semiconductor field effect transistors) Siliconix Incorporated: Punch-through field effect transistor: Second Edition,

## **Power semiconductors - power electronics design**

(BJT), power metal oxide silicon field effect transistors 1997 Temic Semiconductors (formerly Siliconix) Power Electronics (2nd Edition), John Wiley

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## **Patente us6747305 - memory address decode array**

256 such that the pillars operate as metal-oxide-semiconductor field-effect transistors Siliconix Incorporated: Pseudo 2nd Edition, Section: 9

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## **Patent us7977744 - field effect transistor with**

Power field effect transistors, An important factor in designing power devices for high frequency Siliconix Incorporated: Punch-through field effect

**Brevetto us7132712 - trench structure having one**

Trench structure having one or more diodes embedded therein adjacent MOS transistors are applied in designing Siliconix Incorporated: Trenched field effect

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**Patent us7291894 - vertical charge control**

Power field effect transistors, Siliconix Incorporated: Vertical charge control semiconductor device: US20020003266 \*

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**Patent us7652326 - power semiconductor devices and**

Various embodiments for improved power devices as well as their methods of manufacture, entitled Field Effect Transistor and are hereby incorporated by

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low leakage fet. I found my Siliconix FET Design Catalog, July '83. Designing with Field Effect Transistors; 2nd edition Siliconix Ed Oxner

**Brevetto us7511339 - field effect transistor and**

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**Patente us6208164 - programmable logic array with**

Vertical field effect transistors having improved Siliconix Incorporated: Analysis and Design of Digital Integrated Circuits, 2nd Edition, Section

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### **Patent us8518777 - method for forming**

FIGS. 5 and 6 are cross section views of two exemplary trench-gate accumulation-mode vertical field effect transistors Siliconix Incorporated: Second

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[Edwin S Oxner; Siliconix Incorporated.]; Designing with field-effect transistors. New York : McGraw-Hill, " 2nd ed. / " schema:

### **Patent us6078082 - field- effect transistor having**

Field-effect transistors of the MOS type are currently the principal active vehicle This is a second-order effect not reflected in Siliconix Incorporated:

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### **Patent us7598144 - method for forming inter-poly**

The invention relates in general to semiconductor power field effect transistors Siliconix Incorporated: Second Edition,

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(DNA field-effect transistor) is a specialized FET that acts as a biosensor, by using a gate made of single-strand DNA molecules to detect matching DNA strands.

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### **Patente us6710403 - dual trench power mosfet -**

(metal oxide field effect transistors) Designing an equivalent p-channel MOSFET would be Siliconix Incorporated: Trenched field effect transistor with

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### **Patent us8716783 - power device with self-aligned**

A field effect transistor (FET) includes a plurality of trenches extending into a silicon layer, each trench having upper sidewalls that fan out. Contact openings

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