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Fifteenth IEEE/ CHMT International Electronics

Fifteenth IEEE/CHMT International Electronics Manufacturing Technology Symposium : electronics manufacturing for the year 2000 : October 4-6, 1993, Santa Clara, CA, USA

Patent us7531453 - microelectronic devices and

Microelectronic devices, methods for packaging microelectronic devices, and methods for forming interconnects in microelectronic devices are disclosed herein.

Patent us7863187 - microfeature workpieces and

Methods for forming interconnects in microfeature workpieces, and microfeature workpieces having such interconnects are disclosed herein. The microfeature workpieces

Generic interface builder - patent 6708074 by

invention relates generally to manufacturing automation and specifically to software for automation of semiconductor manufacturing of Santa Clara,

Patent us9001486 - internally overlapped

The application discloses novel internal structures of energy conditioners, assemblies of external structures of energy conditioners and mounting structure, and novel

Patent us8536046 - partitioned through-layer via

Partitioned vias, interconnects, and substrates that include such vias and interconnects are disclosed herein. In one embodiment, a substrate has a non-conductive

0780314255 - fifteenth IEEE/ CHMT International

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Patent us7589008 - methods for forming

One aspect of the invention is directed toward a method for manufacturing a microelectronic workpiece having a plurality of microelectronic dies.

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All the Time in the World; Fifteenth IEEE/CHMT International Electronics Manufacturing Technology Symposium

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Previous Titles. Electronic Manufacturing Technology Symposium, 1993, Fifteenth IEEE/CHMT International; Electronic Manufacturing Technology Symposium, 1992.

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Ieee-cpmt International Electronic Manufacturing Technology Symposium: IEEE, Calif.) CHMT International Electronic Manufacturing Technology Symposium (15th : 1993

0780314336 - international electronics

International Electronics Manufacturing Technology Symposium, 1993 Proceedings of Japan IEMT/14th IEEE/CHMT International. 9-11 June, 1993; Kanazawa, Japan.

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Patent us7622377 - microfeature workpiece

Microfeature workpiece substrates having through-substrate vias, and associated methods of formation are disclosed. A method in accordance with one embodiment for

Patent us7915736 - microfeature workpieces and

Methods for forming interconnects in microfeature workpieces, and microfeature workpieces having such interconnects are disclosed herein. In one embodiment, a method

International electronics manufacturing

International Electronics Manufacturing Technology Symposium, 1993 Proceedings of Japan IEMT/14th IEEE/CHMT International. 9-11 June, 1993; Kanazawa, Japan.

Mechatronics education developments in the united

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The mechanics of molded plastic packages -

The Mechanics of Molded Plastic Proc. International Electronics Manufacturing Symp. IEEE-CHMT Proc. International Electronics Manufacturing

Ieee components, packaging, and manufacturing

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Fabtime's wafer fab cycle time bibliography

International Electronics Manufacturing Cycle Time Manufacturing 1993 IEEE/CHMT International

Patent us8247907 - methods for forming

Methods for forming interconnects in microelectronic workpieces and microelectronic workpieces formed using such methods are disclosed herein. One embodiment, for

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View Elliott Newcombe's Thirteenth IEEE/CHMT International Electronics Manufacturing in Consumer Electronics Surface Mount International

Patent us8748311 - microelectronic devices and

Microelectronic devices and methods for filling vias and forming conductive interconnects in microfeature workpieces and dies are disclosed herein. In one embodiment

Patent us6117299 - methods of electroplating

Methods of electroplating solder bumps of uniform height on integrated circuit substrates include the steps of drawing plating current through an integrated circuit

Patent us7795134 - conductive interconnect

Conductive interconnect structures and formation methods using supercritical fluids are disclosed. A method in accordance with one embodiment of the invention

15 results in searchworks

IEEE/CHMT International Electronic Manufacturing Technology Symposium (1988) IEEE/CHMT International Electronic Manufacturing Technology TK7836 .I33 15TH 1993

Intelligent manufacturing systems (smart

The Smart Mechatronics/Intelligent Manufacturing Research initiative at Louisiana State University 15th IEEE /EIA Components (CHMT) International Electronics

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Patent us7829976 - microelectronic devices and

Microelectronic devices, methods for packaging microelectronic devices, and methods for forming interconnects in microelectronic devices are disclosed herein. In one

Patent us8536485 - systems and methods for forming

Systems and methods for forming apertures in microfeature workpieces are disclosed herein. In one embodiment, a method includes directing a laser beam toward a

Chandrasekhar (spike) narayan - publications

San Jose, CA, USA +1 408 927 2405. Annual International Reliability Physics Symposium, Phoenix for CMOS BEOL Interconnect Technology C. Narayan

Patent us7749899 - microelectronic workpieces and

Methods and systems for forming electrical interconnects through microelectronic workpieces are disclosed herein. One aspect of the invention is directed to a method

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